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(Use several sheets if necessary)		Applicants Yasuhiro SUZUKI, et al.	Page 1 of 1
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub Class	<u>Translation</u>	
							YES	NO
		02/012845 A1	Feb. 14, 2002	WIPO			Abstract	
		2000-310561 A	Nov. 7, 2000	Japan			Abstract	
		2001-291877 A	Oct. 19, 2001	Japan			Abstract	
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		63-205527 A	Aug. 25, 1988	Japan			Abstract	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

S.L. Garverick, et al., "A 32-Channel Charge Readout IC for Programmable, Nonlinear Quantization of Multichannel Detector Data", IEEE Journal of Solid-State Circuits, Vol. 30, No. 5, pp 533-541, 1995

Examiner	/Tony Ko/	Date Considered	12/07/2008
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